



The diagram shows a cross-sectional view of a semiconductor device. A substrate 16 is shown with a top layer 16b and a bottom layer 16a. A series of five shaded regions 80 are located on the bottom layer 16a. A line 4 is connected to a block 8. A ground symbol 7 is connected to the bottom layer 16a.

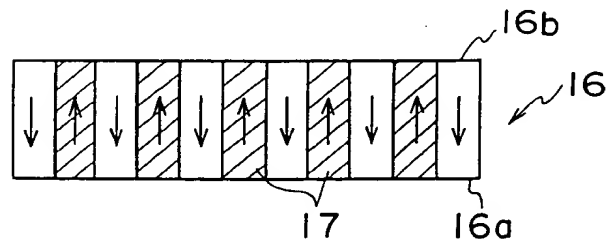
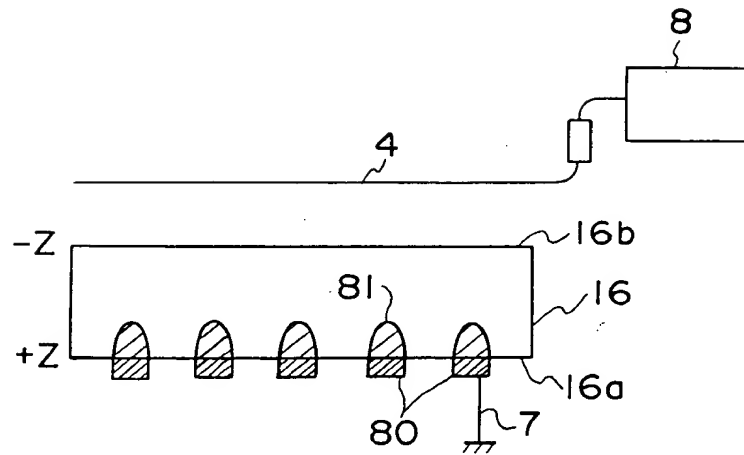




FIG. 4

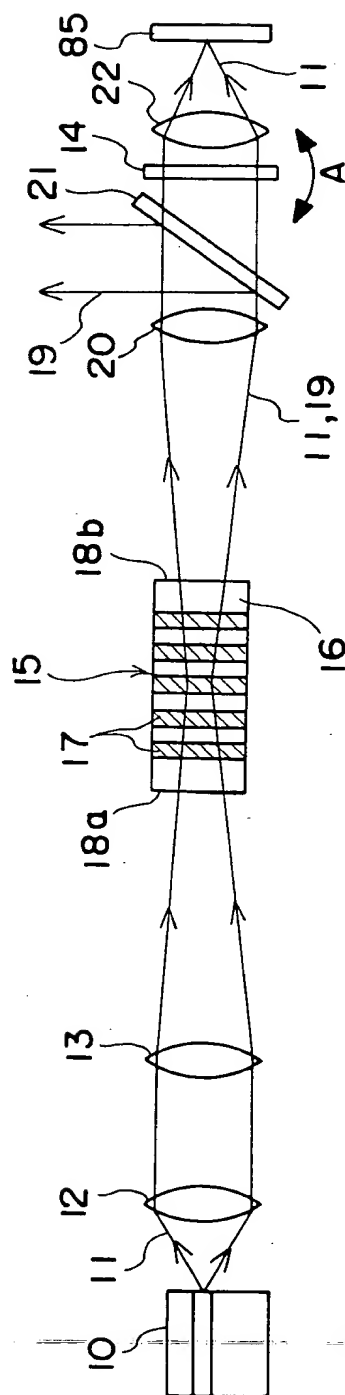


FIG. 5A

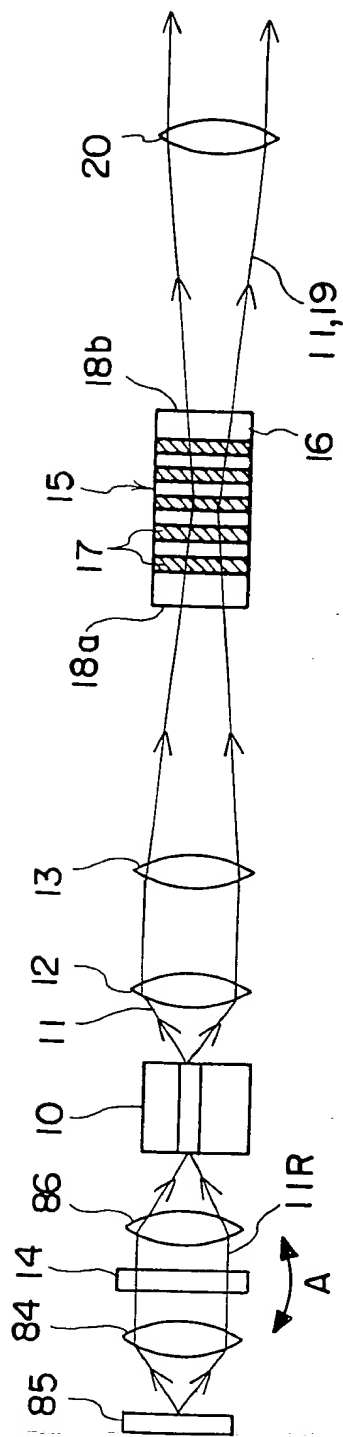


FIG. 5B

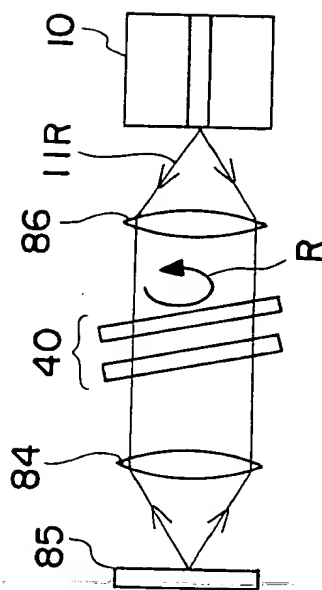


FIG. 6A

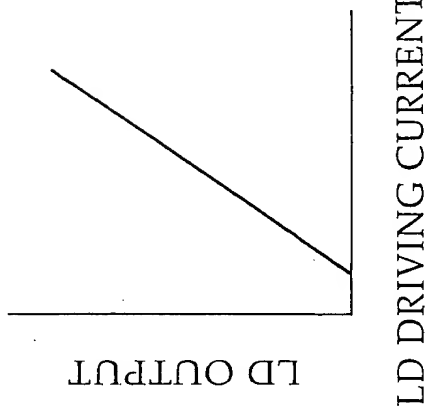


FIG. 6B

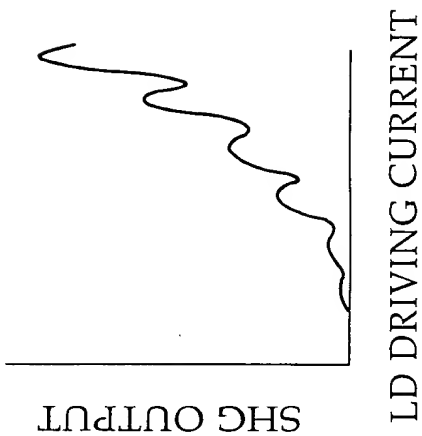




FIG. 8

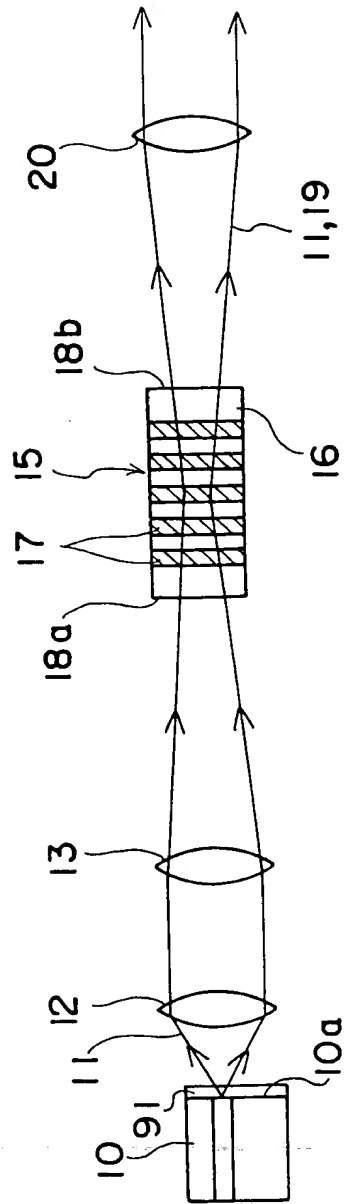
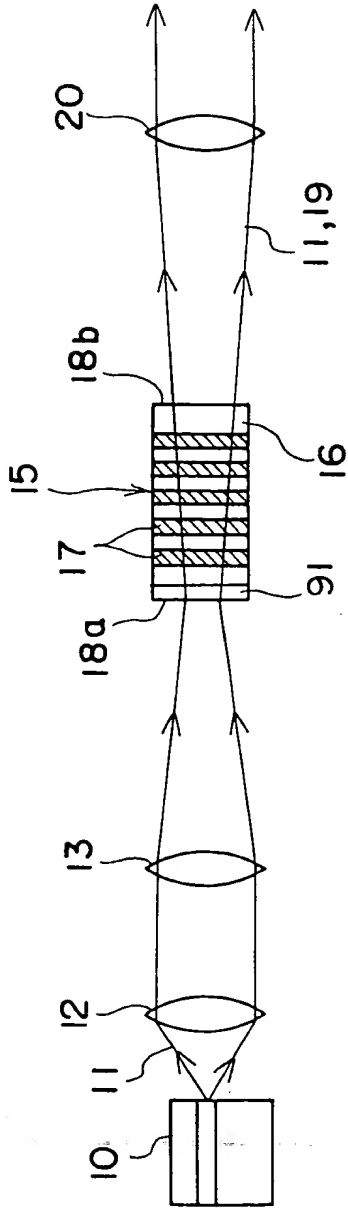




FIG. 9



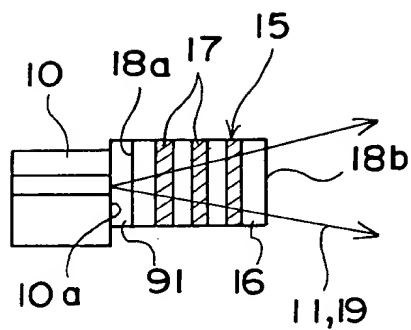


FIG. 11

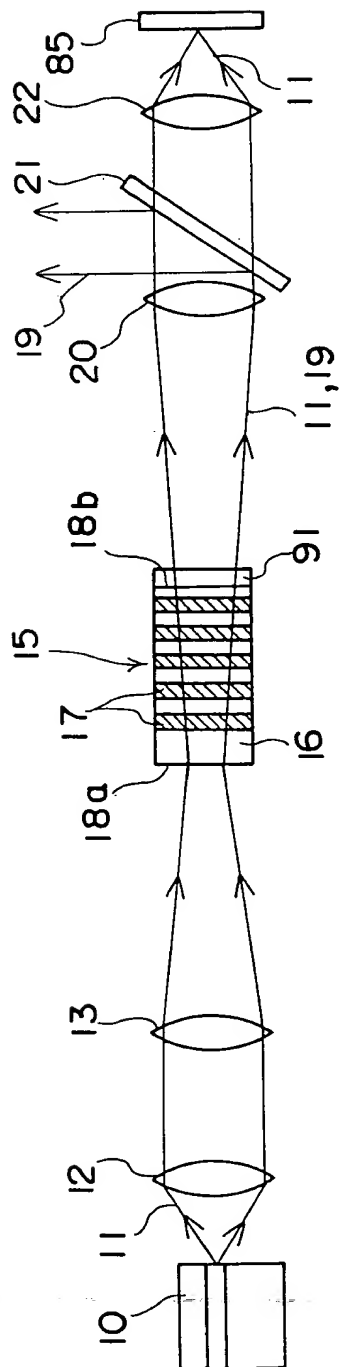


FIG. 12

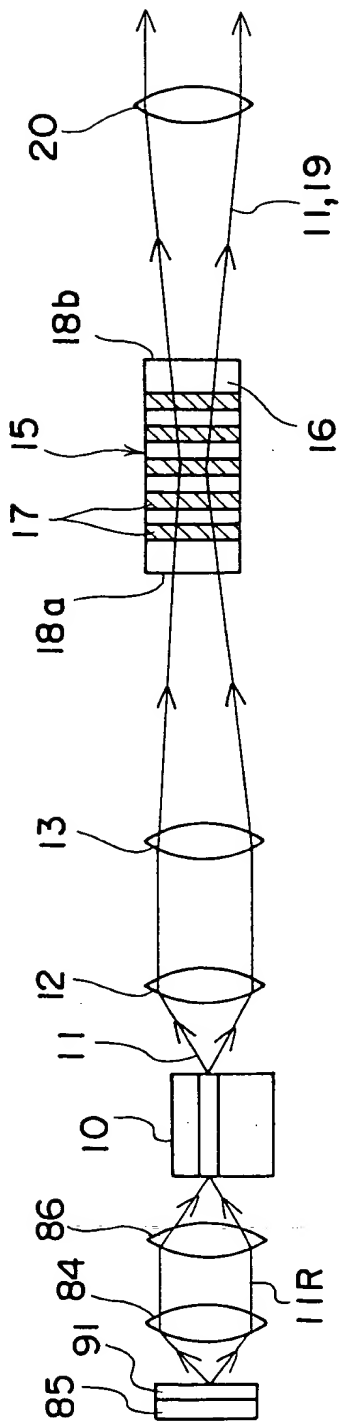


FIG. 13

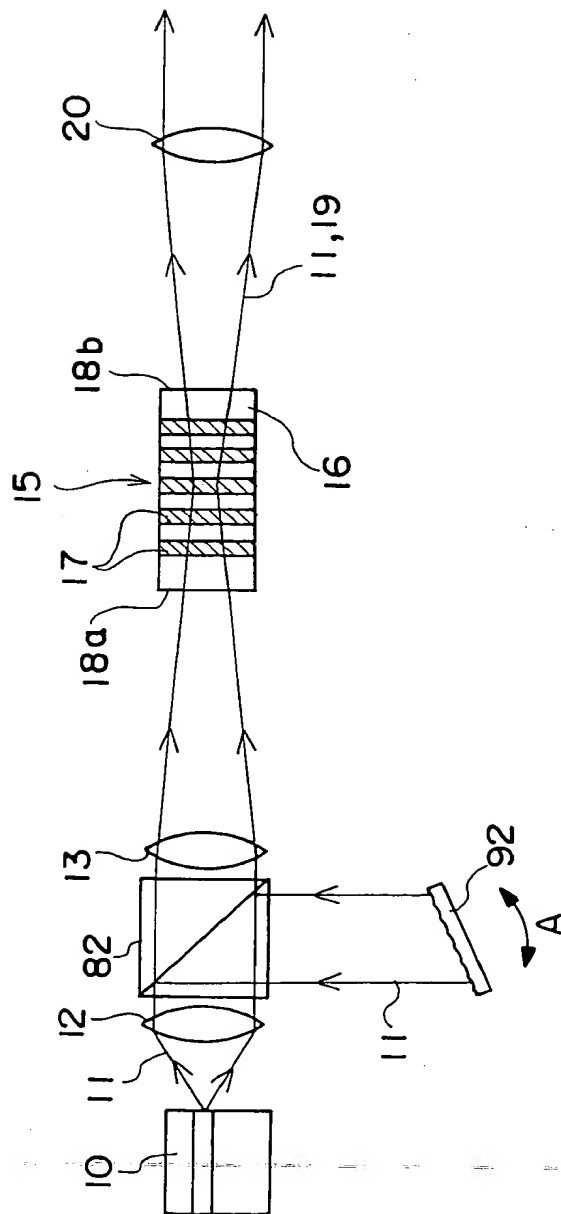






FIG. 16

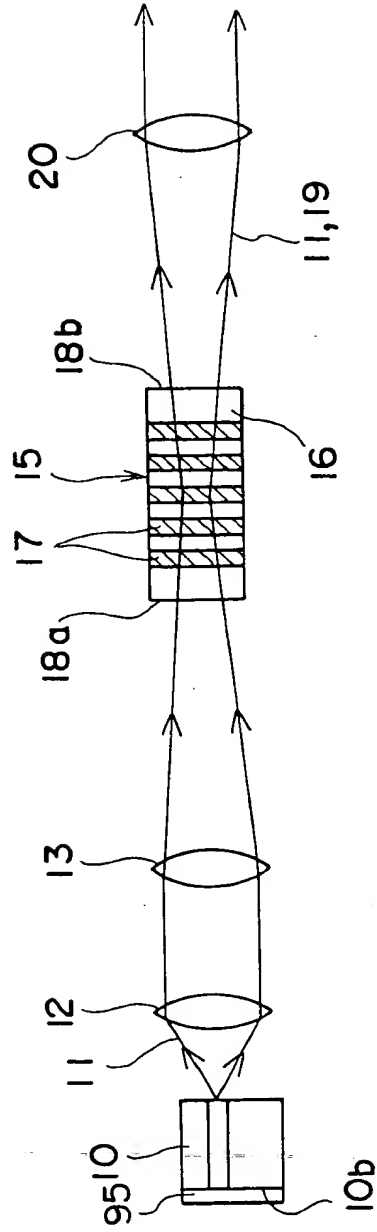






FIG. 18

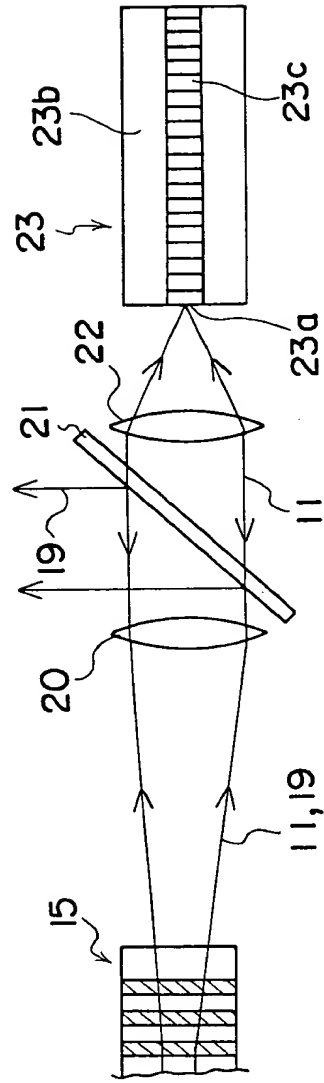






FIG. 21

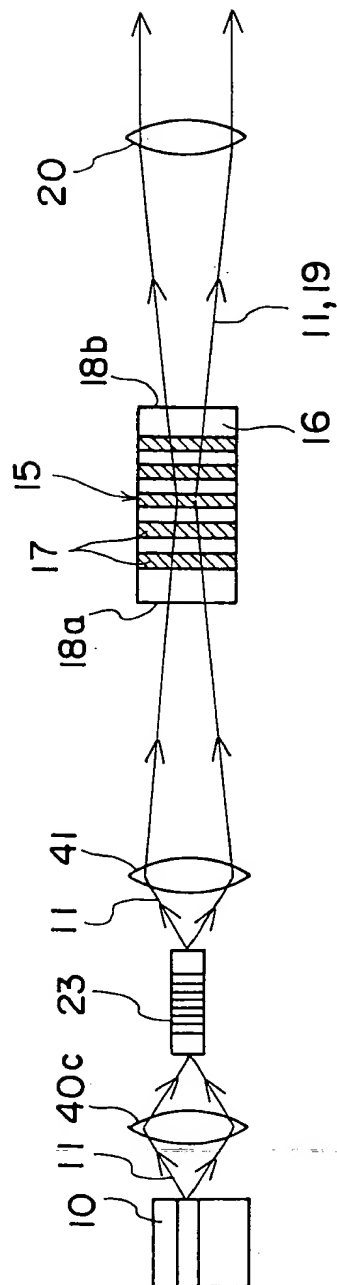


FIG. 22

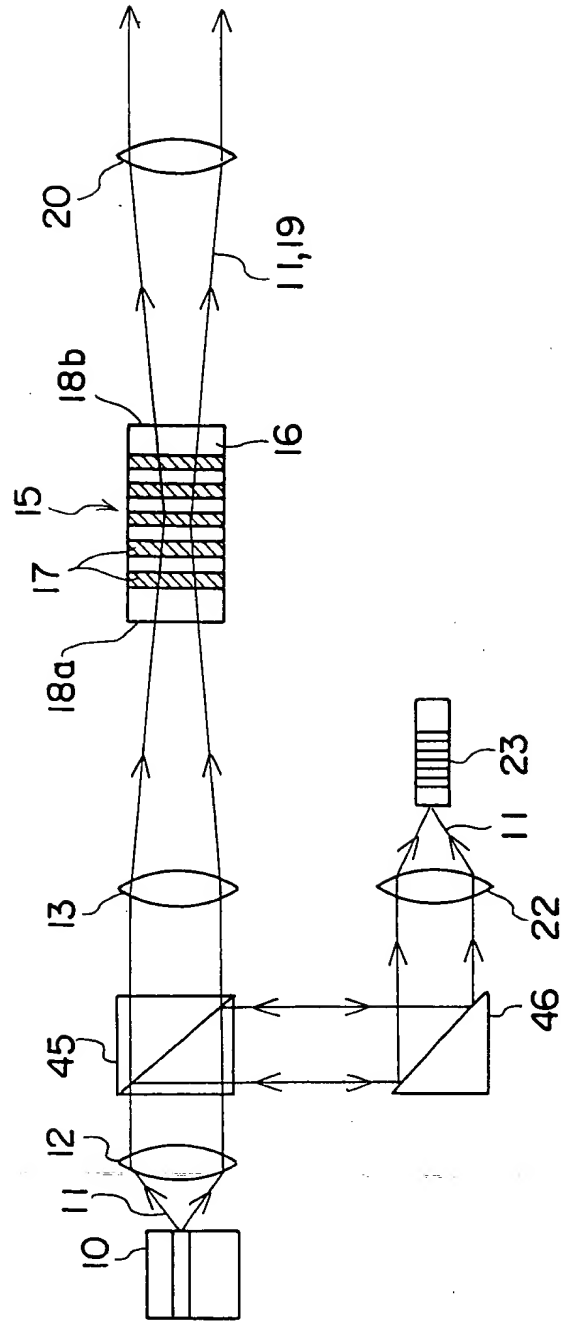


FIG. 23

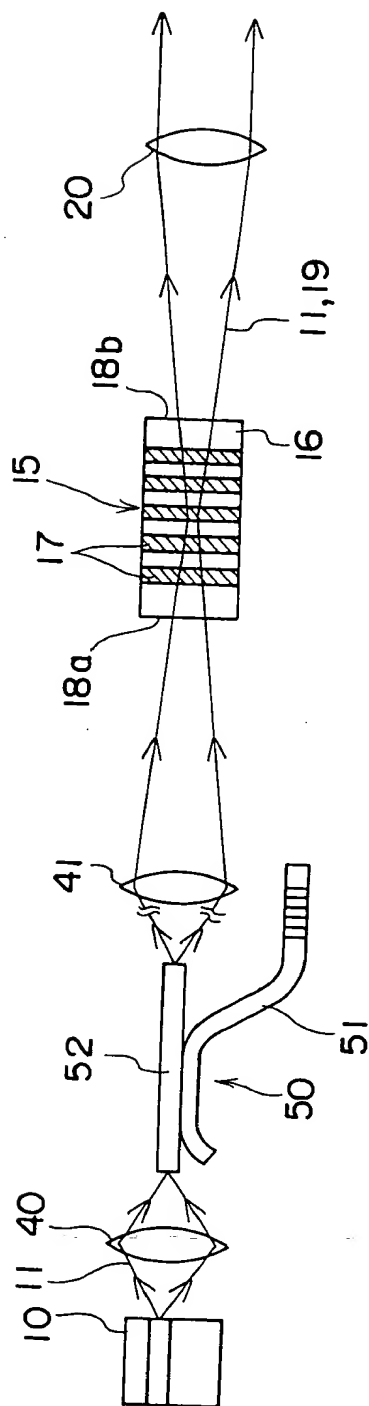


FIG. 24

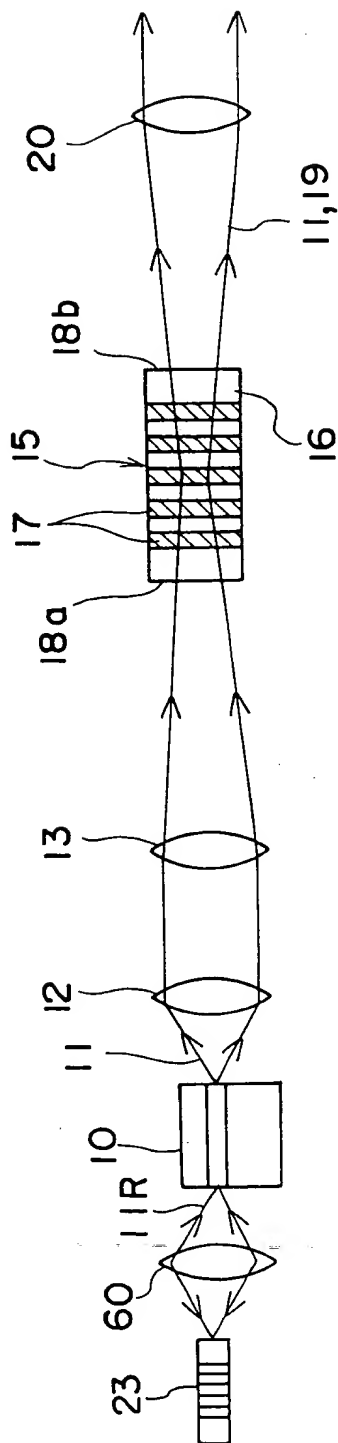




FIG. 25

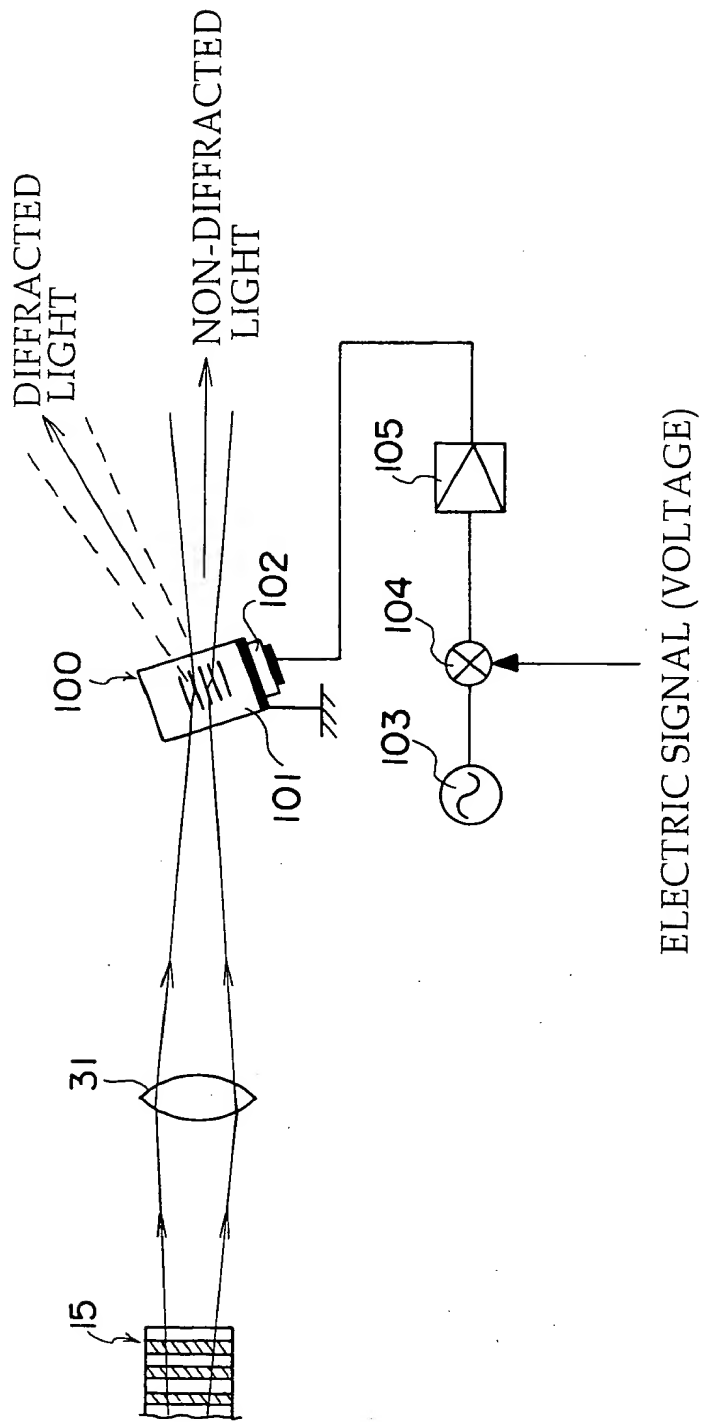


FIG. 26

